

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	443	core adj controller or core adj processor	USPAT	2001/03/02 15:57
2	BRS	L2	3190	graphic\$ adj controller or graphic\$ adj processor	USPAT	2001/03/02 15:59
3	BRS	L3	14128	memory adj controller or memory adj interfac\$	USPAT	2001/03/02 16:06
4	BRS	L4	2976	bus adj bridge or core adj logic or chipset	USPAT	2001/03/02 16:09
5	BRS	L5	1000	integrated near5 graphic\$	USPAT	2001/03/02 16:41
6	BRS	L6	22	1 and 2	USPAT	2001/03/02 16:11
7	BRS	L7	149	1 and 3	USPAT	2001/03/02 16:11
8	BRS	L8	65	1 and 4	USPAT	2001/03/02 16:11
9	BRS	L10	959	2 and 3	USPAT	2001/03/02 16:12
10	BRS	L11	354	2 and 4	USPAT	2001/03/02 16:12
11	BRS	L12	178	10 and 11	USPAT	2001/03/02 16:13
12	BRS	L13	51	7 and 8	USPAT	2001/03/02 16:13
13	BRS	L14	13	6 and 3	USPAT	2001/03/02 16:13
14	BRS	L16	9	14 and 15	USPAT	2001/03/02 16:14
15	BRS	L15	9	6 and 4	USPAT	2001/03/02 16:20
16	BRS	L17	4	14 not 15	USPAT	2001/03/02 16:20
17	BRS	L9	15	5 same 4	USPAT	2001/03/02 16:31
18	BRS	L19	9	6 not 14	USPAT	2001/03/02 16:33
19	BRS	L18	42	13 not 14	USPAT	2001/03/02 16:39
20	BRS	L20	0	1 and 2 and 3 and 4 and 5	USPAT	2001/03/02 16:39
21	BRS	L21	9	1 and 2 and 3 and 4	USPAT	2001/03/02 16:39
22	BRS	L22	1567	integrat\$ near5 graphic\$	USPAT	2001/03/02 16:44
23	BRS	L23	34	22 near8 (chipset or logic)	USPAT	2001/03/02 16:46